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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Manjunath Haritsa, Manishkumar Ankola, Ralf M. Schmitt, Anup Sharma, Stephan Hoerold, David M. Murata

Assignee: Sun Microsystems, Inc.

Title: CLOCK SKEW VERIFICATION METHODOLOGY FOR GRID-BASED DESIGN

Serial No.: 09/982,452 Filing Date: October 17, 2001

Examiner: Binh C. Tat Group Art Unit: 2825

Docket No.: P-5403

Monterey, CA
March 22, 2005

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

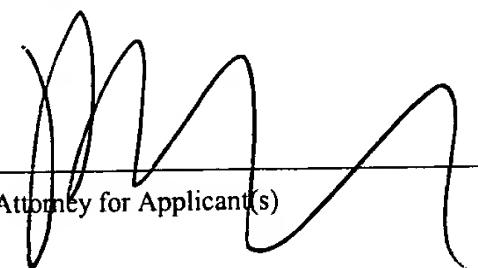
STATUS INQUIRY

Dear Sir:

The last paper received by Applicant(s) is the paper mailed August 25, 2004. What is the expected date for a response to our Amendment filed on December 21, 2004?

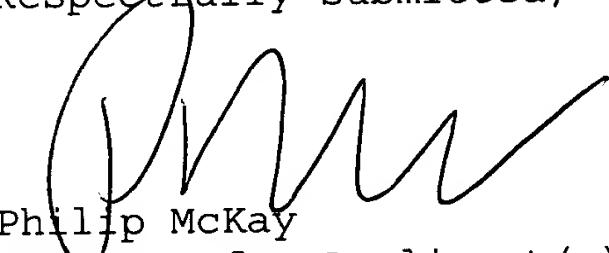
CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on March 22, 2005.


Attorney for Applicant(s)

March 22, 2005
Date of Signature

Respectfully submitted,


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